

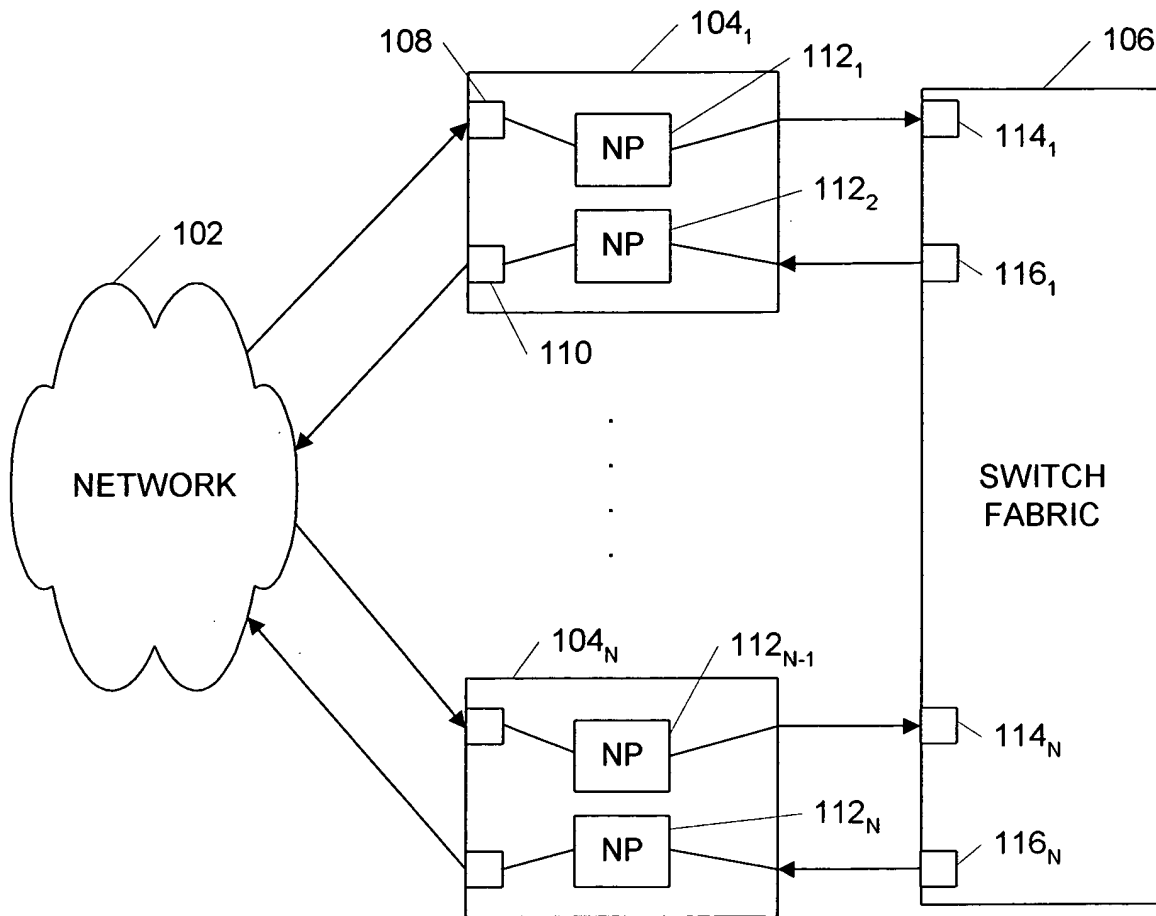
100

FIG. 1

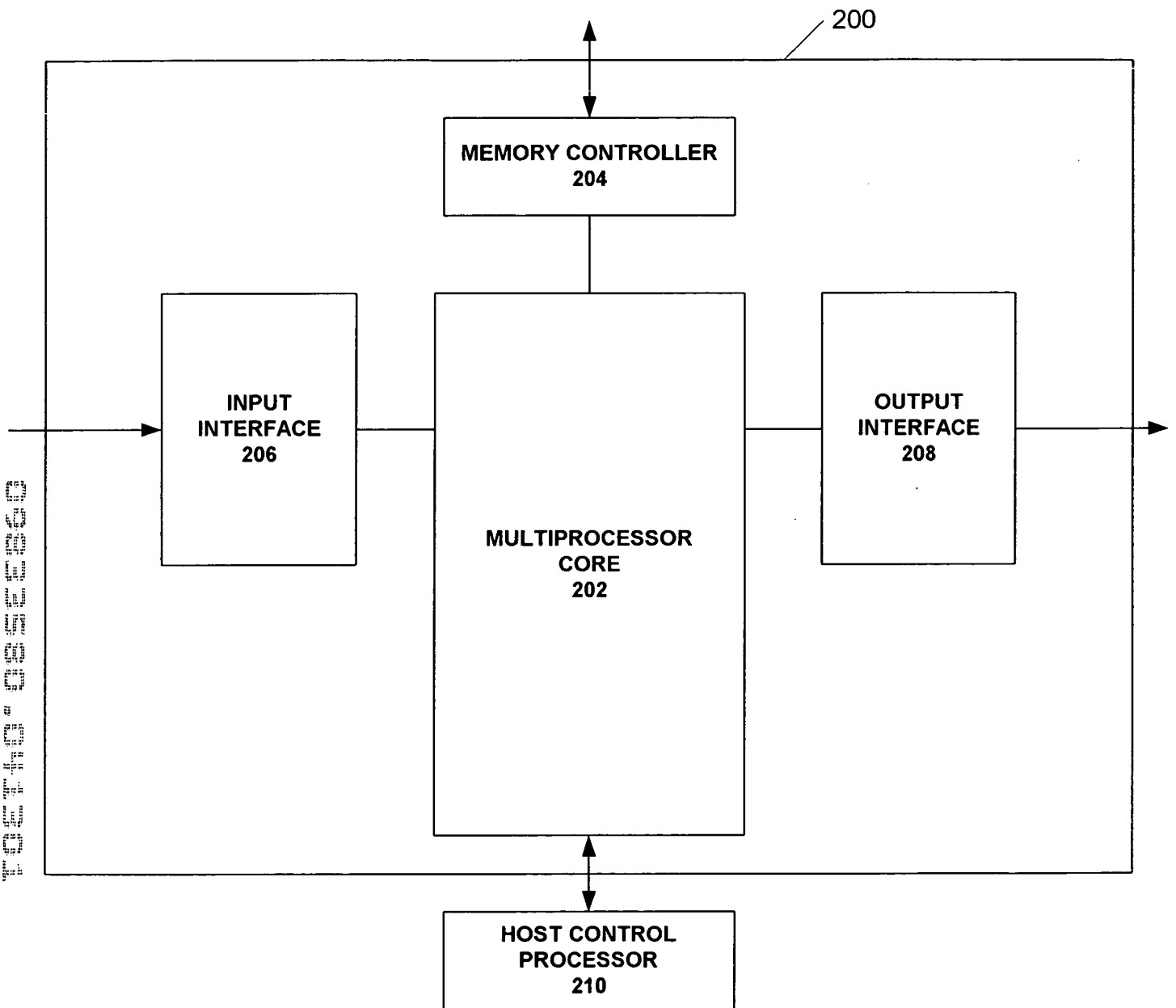
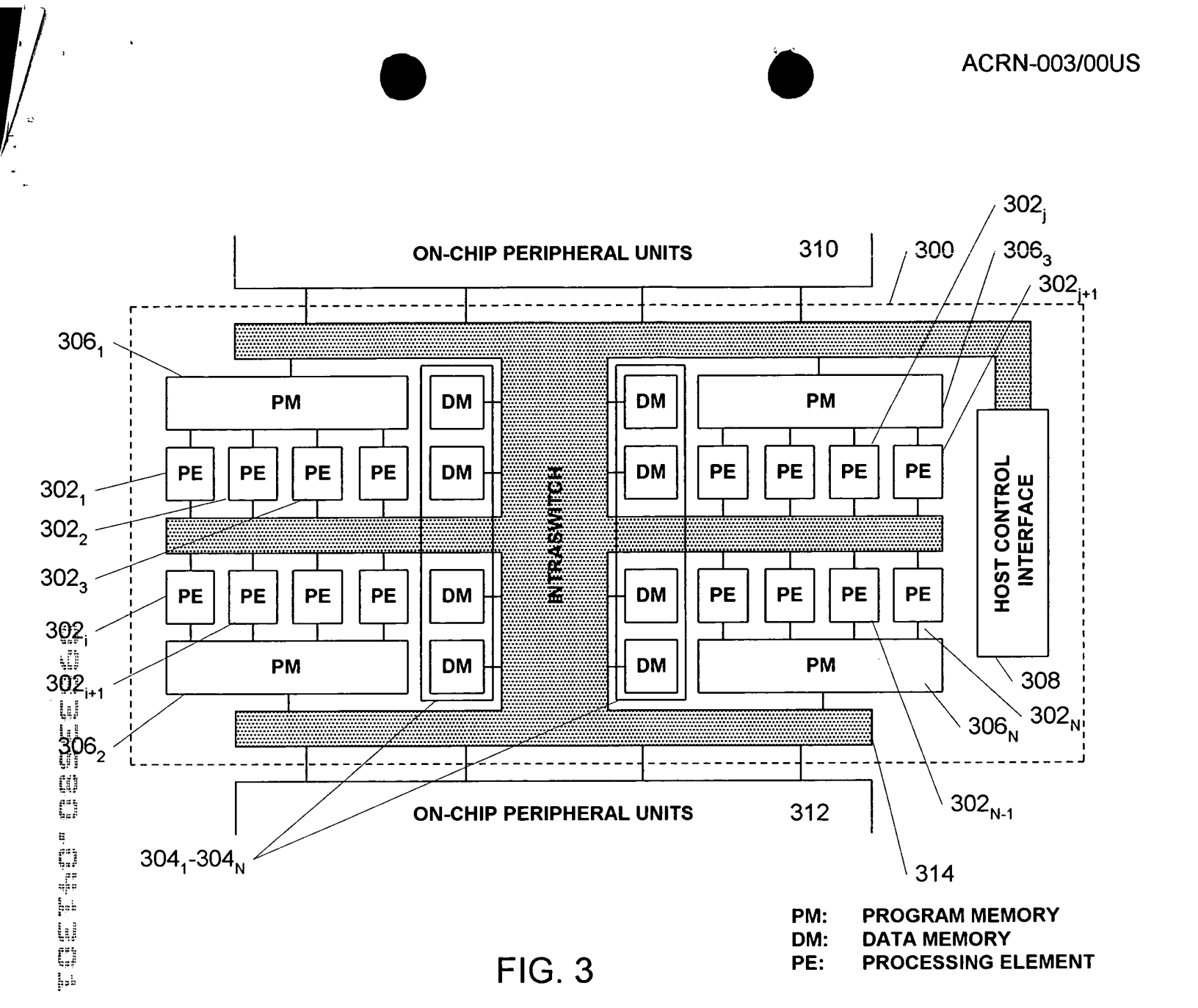


FIG. 2



ON-CHIP PERIPHERAL UNITS 310

300

306₁

302₁

302₂

302₃

302_i

302_{i+1}

306₂

304₁-304_N

ON-CHIP PERIPHERAL UNITS 312

302_{N-1}

302_N

306_N

314

PM

DM

PE

INTRASWITCH

HOST CONTROL INTERFACE 308

PM: PROGRAM MEMORY
DM: DATA MEMORY
PE: PROCESSING ELEMENT

FIG. 3

FIG. 3

PM: PROGRAM MEMORY
DM: DATA MEMORY
PE: PROCESSING ELEMENT

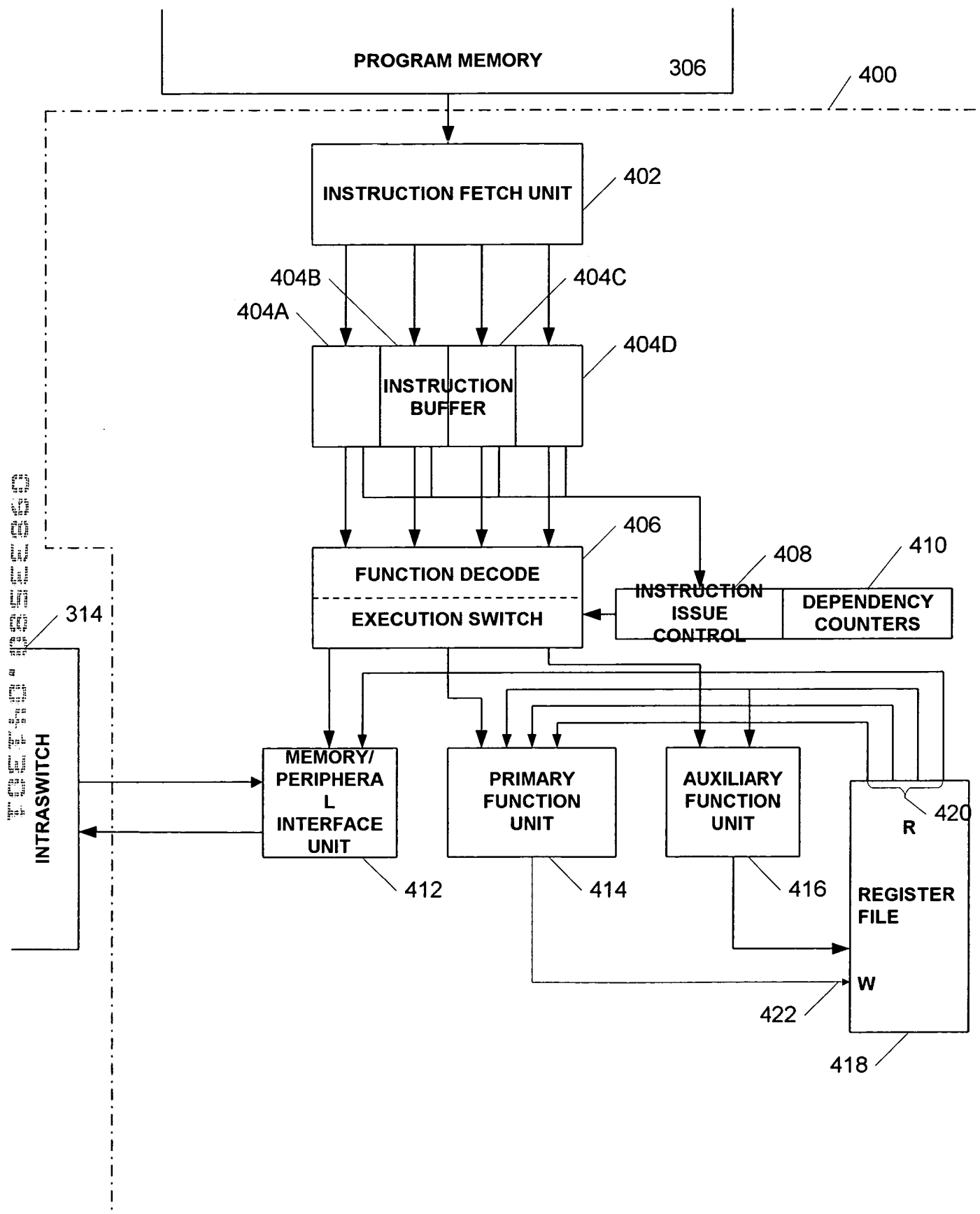


FIG. 4

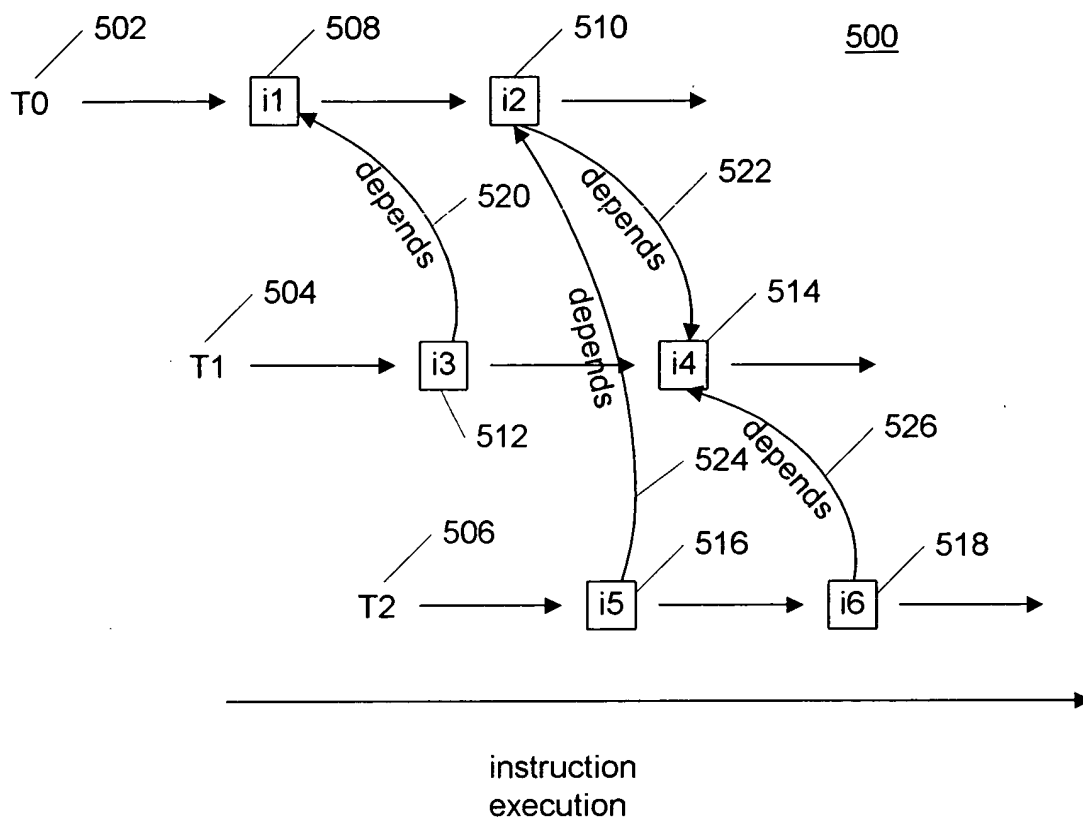


FIG. 5

600

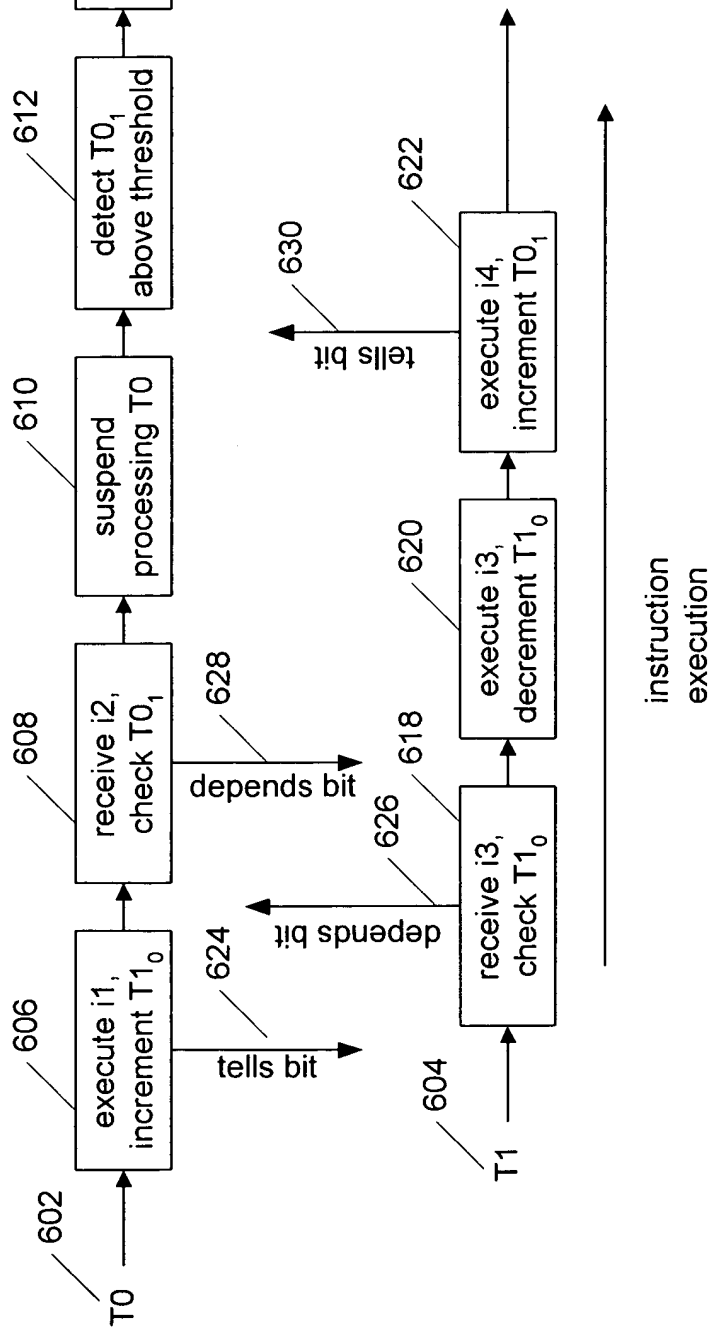


FIG. 6

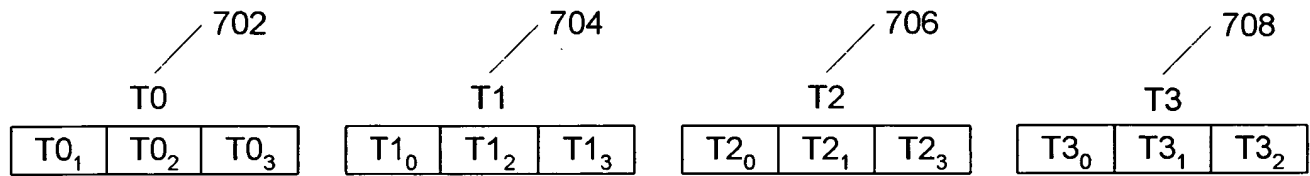


FIG. 7

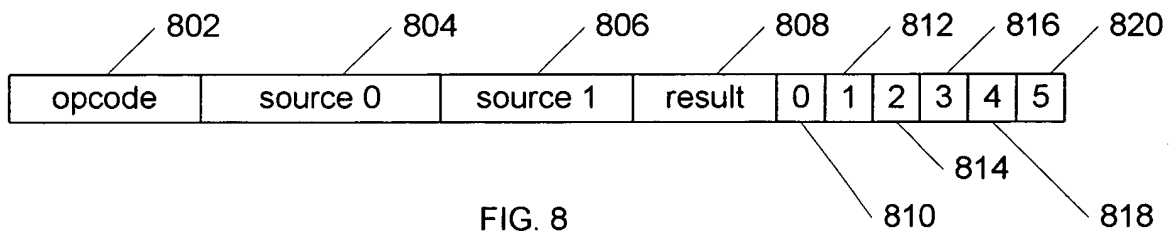
800

FIG. 8

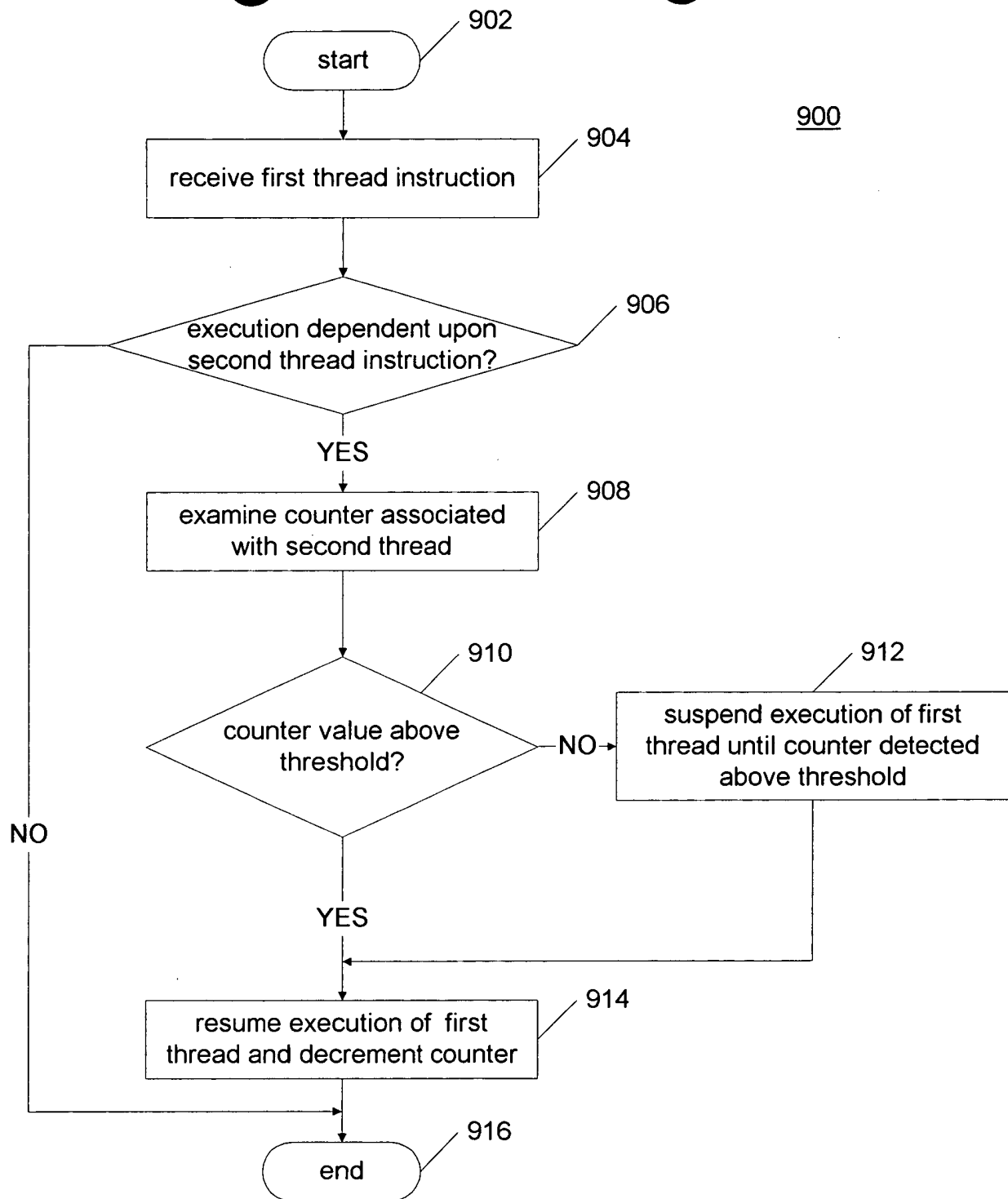


FIG. 9

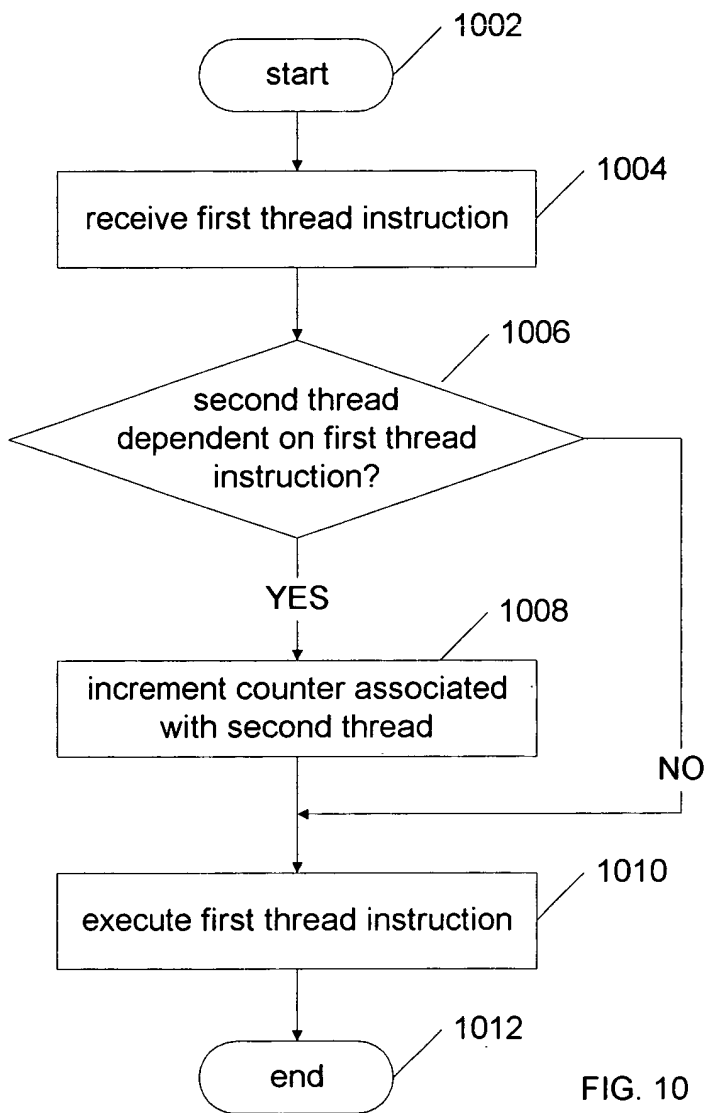


FIG. 10